



Docket No.: 60188-429

8/A
6/23/03
Mallish
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shigetak AOKI

Serial No.: 10/067,866

Group Art Unit: 2811

Filed: February 8, 2002

Examiner: S. Gebremariam

For: SEMICONDUCTOR DEVICE

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED
JUN 18 2003
TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Office action dated February 13, 2003, having a shortened statutory response period set to May 13, 2003, a one month extension of time up to June 13, 2002 being filed herewith, please amend the above application as follows:

IN THE CLAIMS:

✓
Please cancel claims 2, 3 and ~~11~~ without prejudice or disclaimer.

Please amend claims 1, 5 and 9 and add new claim 11 as follows:

1. (Amended) A semiconductor device, comprising:

a substrate having a first semiconductor layer;

A1
a second semiconductor layer provided on the first semiconductor layer, wherein the second semiconductor layer has a smaller band gap than the first semiconductor layer and is made of a mixed crystal semiconductor; and